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said memory;

a memory controller for controlling transfer of data between said memory and said data processor and between said memory and a display;

a first bus, having m (wherein m is an integer) bits width, connected between said memory and said memory controller for transferring m bits of data in parallel; and

a second bus, having n (wherein n is an integer and $n > m$) bits width, connected between said memory controller and said data processor, for transferring n bits of data in parallel,

wherein said memory controller comprises:

at least one bit terminal, connected to said display, for transferring serial data between said display and said memory,

first converting means for performing conversion between data of plural sets of m bits via said first bus and data of n bits via said second bus based on an indication from said data processor, and

second converting means for converting said data of plural sets of m bits via said first bus into said serial data. W

REMARKS

Applicants' Attorney, the undersigned, wishes to thank Examiner Zimmerman for the courtesy extended during the interviews of March 20, 2000 and March 21, 2000 concerning the above-referenced application.

According to the interview held on March 20, 2000, Examiner Zimmerman and Applicants' Attorney discussed the status of the Appeal including the rejections of the claims being appealed to the Board. It was noted during the interview that claims 44-58 and 63-66 stand rejected under 35 USC §251 as being based on an alleged defective declaration and under 35 USC §103 as being unpatentable over the Graciotti, Tanenaka and Pinkham references, and that claims 59-62 stand rejected only under 35 USC §251 as being based on the alleged defective declaration. Examiner Zimmerman was informed of Applicants desire to expedite the prosecution of the present application since the application has been pending before the United States Patent and Trademark Office since December 3, 1992.

Therefore, Applicants' Attorney proposed placing the application in immediate condition for allowance by canceling claims 44-58 and 63-66 and overcoming the non-prior art rejection of claims 59-62.

Examiner Zimmerman acknowledging Applicants desire to expedite prosecution of the present application agreed that the present application would be allowed if Applicants prepare and submit an Amendment canceling claims 44-58 and 63-66 since these claims are rejected under 35 USC §251 and 35 USC §103, thereby leaving claims 59-62 which were only rejected under 35 USC §251, and prepare and submit a new Supplemental Reissue Declaration to overcome the 35 USC §251 rejection of claims 59-62. Entry of such an Amendment is appropriate under 37 CFR §1.116(b) since the Amendment would

resolve all issues and place the application in condition for allowance. Further the Amendment could not have been earlier presented since it was Applicants' expectation that the Appeal, first filed on June 8, 1998, would be conducted expeditiously.

Accordingly, the present Amendment cancels claims 44-58 and 63-66 and submits a new Supplemental Reissue Declaration attached herewith so as to place the present application in immediate condition for allowance. Examiner Zimmerman stated during the March 20, 2000 interview that upon receipt of such papers the application will be immediately allowed since no other outstanding issues would remain.

It should be noted that Applicants are not disclaiming or abandoning the subject matter set forth in claims 44-58 and 63-66. It is still Applicants position that the subject matter of these claims are patentable over the prior art of record. The present Amendment is merely being presented to expedite the prosecution of the present application. Claims to the subject matter set forth in claims 44-58 and 63-66 may be the subject of a continuation application to be filed at a later date.

Subsequent to the March 20, 2000 interview, a further interview was held with Examiner Zimmerman on March 21, 2000. During the March 21, 2000 interview Applicants' Attorney expressed Applicants concern that canceling all of claims 44-58 and 63-66, to expedite the prosecution of the present application, would cause the application to issue without any system claims. Therefore, in this regard,

Examiner Zimmerman was requested to indicate how he would treat system claims directed to a graphic processing apparatus having a memory, a data processor and a memory controller which includes the patentable features of the memory controller recited in, for example, claim 59.

Examiner Zimmerman stated that an Amendment including such system claims would be entered and such system claims would be allowed since they would contain the patentable features of the memory controller recited in claim 59.

Therefore, in this regard, the present Amendment adds two new system claims 67 and 68 each of which describes a graphic processing apparatus having a memory, a data processor and a memory controller including the patentable features of the memory controller recited in claim 59.

For example, claim 67 recites a graphic processing apparatus including a memory, data processor and memory controller. The memory controller includes m-bit terminals, an n-bit interface, at least one bit terminal, first converting means and second converting means essentially the same as that recited in the claim 59.

Claim 68 recites a graphic processing apparatus including a memory, a data processor, a memory controller, a first data bus having m-bits width and a second data bus having n-bits width.

The first bus having m-bits width corresponds to the m-bit terminals of the memory controller recited in claim 59 and the second bus having n-bits width corresponds to the n-bit interface of the memory controller recited in claim 59.

The other elements of the memory controller recited in claim 59 can be found in claim 68 which recites that the memory controller includes the at least one bit terminal, the first converting means and the second converting means. Thus, claim 68 although somewhat rearranged relative to claim 59, essentially recites the same features of the memory controller recited in claim 59.

Therefore, based on the agreement reached with Examiner Zimmerman during the interview of March 21, 2000, both of claims 67 and 68 are allowable being that they are system claims each including the patentable features of the memory controller recited in claim 59.

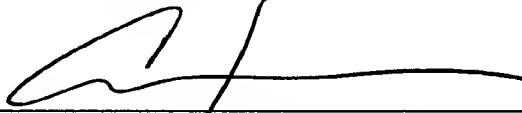
Examiner Zimmerman is encouraged to contact Applicants' Attorney by telephone in order to discuss any further issues regarding the present Amendment and the prosecution of the present application.

In view of the foregoing amendments and remarks, Applicants submit that the present application is now in condition for allowance based on claims 59-62, 67 and 68. Accordingly, early allowance of present application based on claims 59-62, 67 and 68 is respectfully requested.

To the extent necessary, applicants petition for an extension of time under 37 C.F.R. section 1.136. Please charge any shortage in the fees due in connection with the

filing of this paper, including extension of time fees, to
Deposit Account No. 01-2135 (Case No. 500.26967R00) and
please credit any excess fees to such Deposit Account.

Respectfully submitted,



Carl I. Brundidge
Registration No. 29,621
ANTONELLI, TERRY, STOUT & KRAUS, LLP

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(703) 312-6600